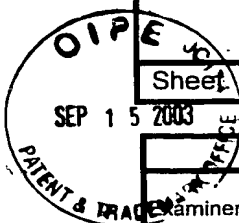


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		Filing Date	June 5, 2000
		First Named Inventor	VINITZKY, Gil
		Group Art Unit	2124
		Examiner Name	DO, Chat C.
		Attorney Docket Number	P-2596-US



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CD	A	MARSHALL C. PEASE, "Organization of Large Scale Fourier Processors", Journal of the Association for Computing Machinery, Vol. 16, No. 3, July 1969, pp. 474-482	<input type="checkbox"/>
CD	B	YUTAI MA et al., "A Hardware Efficient Control of Memory Addressing for High-Performance FFT Processors", IEEE Transactions on Signal Processing, Vol. 48, No. 3, March 2000, pp. 917-921	<input type="checkbox"/>
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